

Exploring Next Token Prediction For Optimizing Databases

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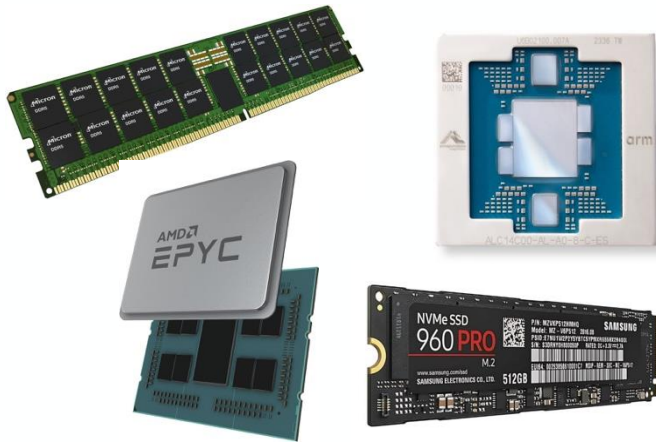


Roadmap

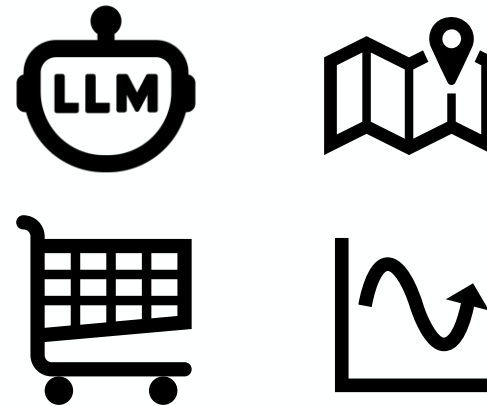
- The current landscape of Database Systems design space
 - Hardware and Workload
- The LLM recipe
 - Next Token Prediction (NTP): First step towards adopting the LLM recipe
- The building blocks to adopt Next Token Prediction (NTP) in databases
 - Decision Transformers
 - DB-Tokens
- The Probe and Learn (PoLe) framework
 - Preliminary case study
 - Index Scheduling

The Game Changers in Databases ^[1]

Hardware



Workload



[1] Anastasia Ailamaki. 2021. Accelerated Data Management Systems Through Real-Time Specialization. Keynote at MICRO.

The current state

Hardware

Workload




What's the current state?

The Current State: Observation 1

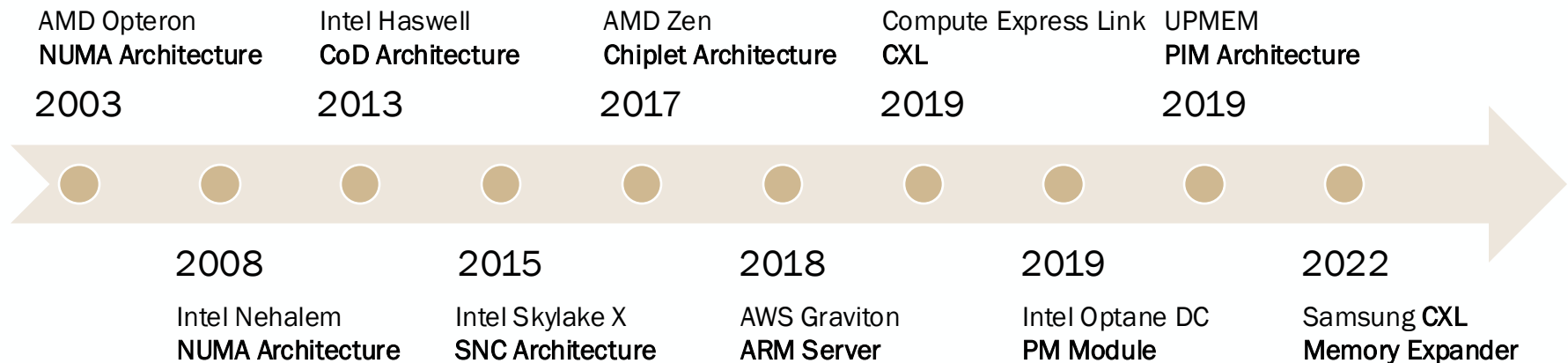
Hardware

Workload

- 
- A background collage featuring various hardware components like RAM modules and SSDs on the left, and icons representing workload such as a server rack, a shopping cart, and a location pin on the right.
1. Both the hardware and workload are **rapidly evolving.**

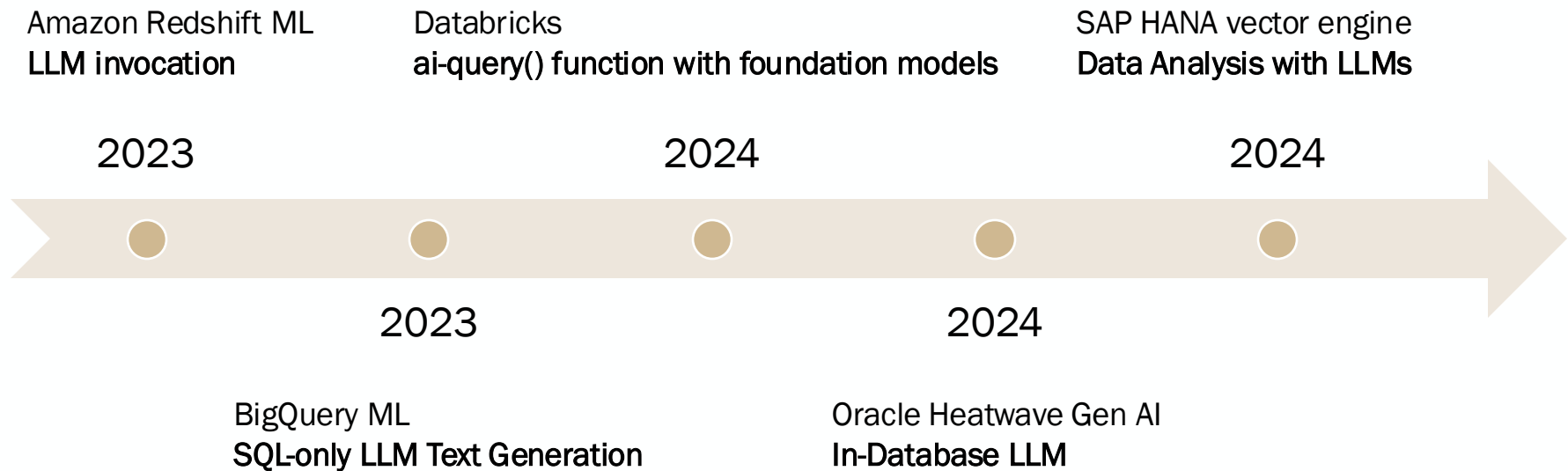
Observation 1: Rapid Evolution of Hardware

- Every few years, there is a new technology:
 - Compute, Memory and Storage
- Within the past 6 years, we have seen the commercial introduction of
 - Processing In Memory (PIM) chips by UPMEM
 - Compute Express Links (CXL)
 - ARM-based server processors



Observation 1: Rapid Evolution of Workload

- The applications that databases need to support are continuously growing.
- Within the past 3-4 years, we have seen a drastic shift in the ML workloads.
 - Large Language Models (LLM)



The current state

Hardware

Workload



What's the current state?

The Current State: Observation 2

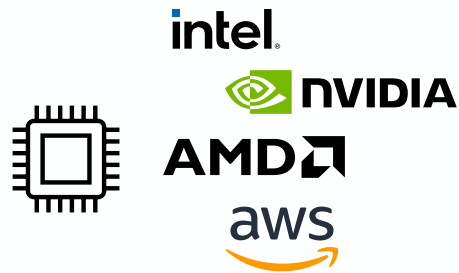
Hardware

Workload

A background collage featuring various hardware components like RAM modules and SSDs, alongside icons representing different workloads such as a server rack, a shopping cart, and a location pin.

2. Both the hardware and workload are **heterogeneous.**

Heterogeneity in Hardware



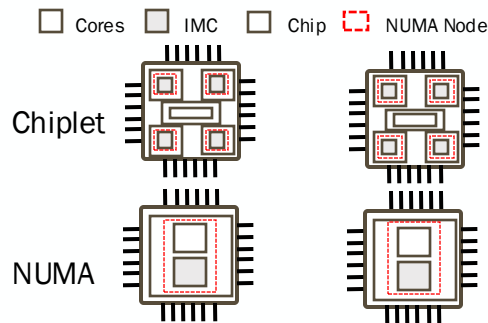
Different Vendors



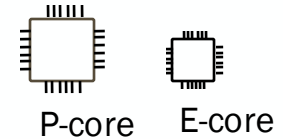
SAMSUNG
ASUS
micron

x86
RISC IBM Power RISC-v

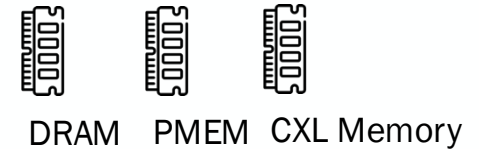
Different Processor Architecture



NUMA VS Chiplet Architecture



Compute Cores



Memory

Heterogeneity in Workload



ML Workload

LLM Queries
Training Data Preparation
Real-time Prediction



Spatial Workload

Location based Services
Geographic Information Systems
Moving Objects



OLTP

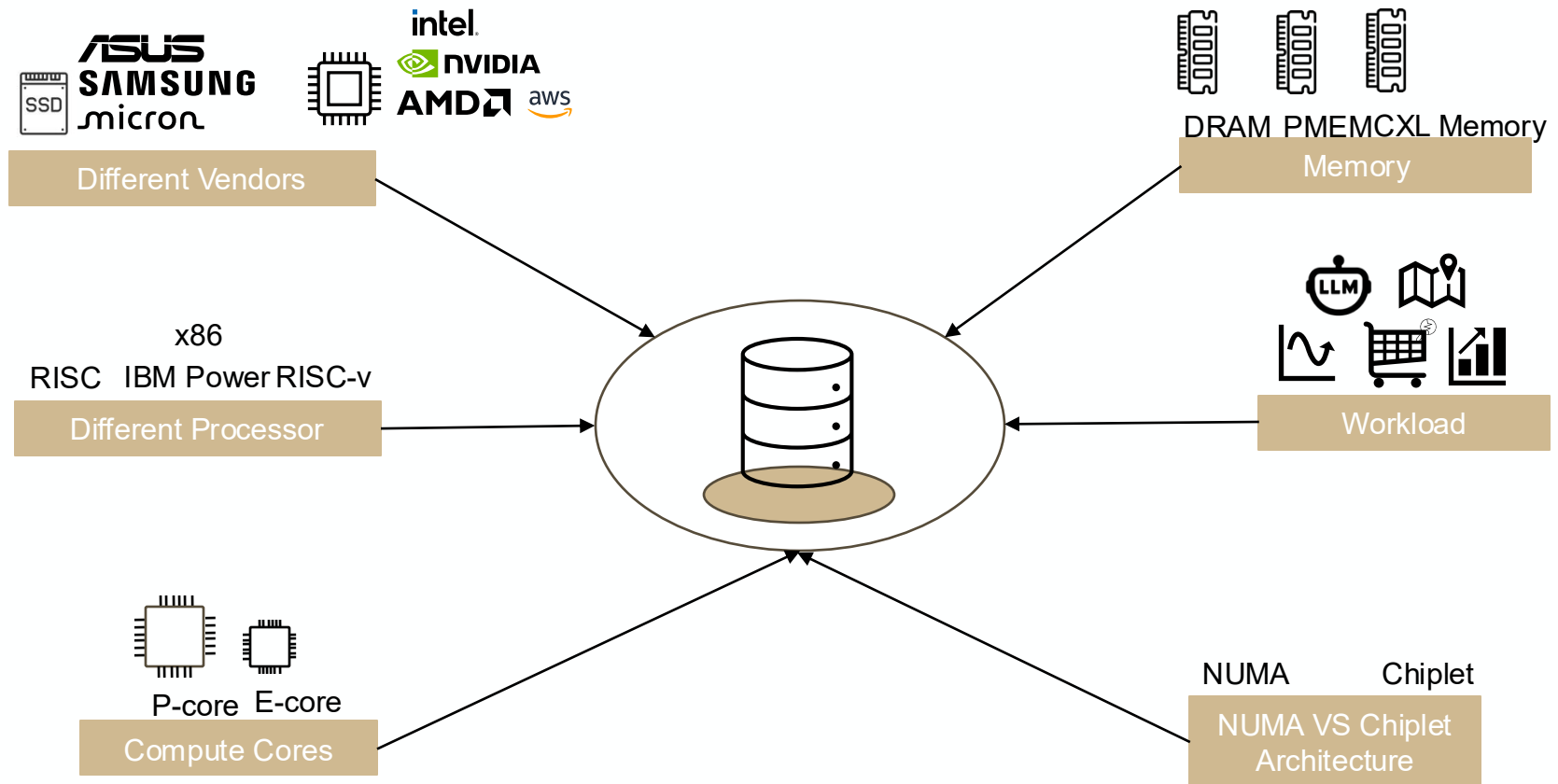


HTAP

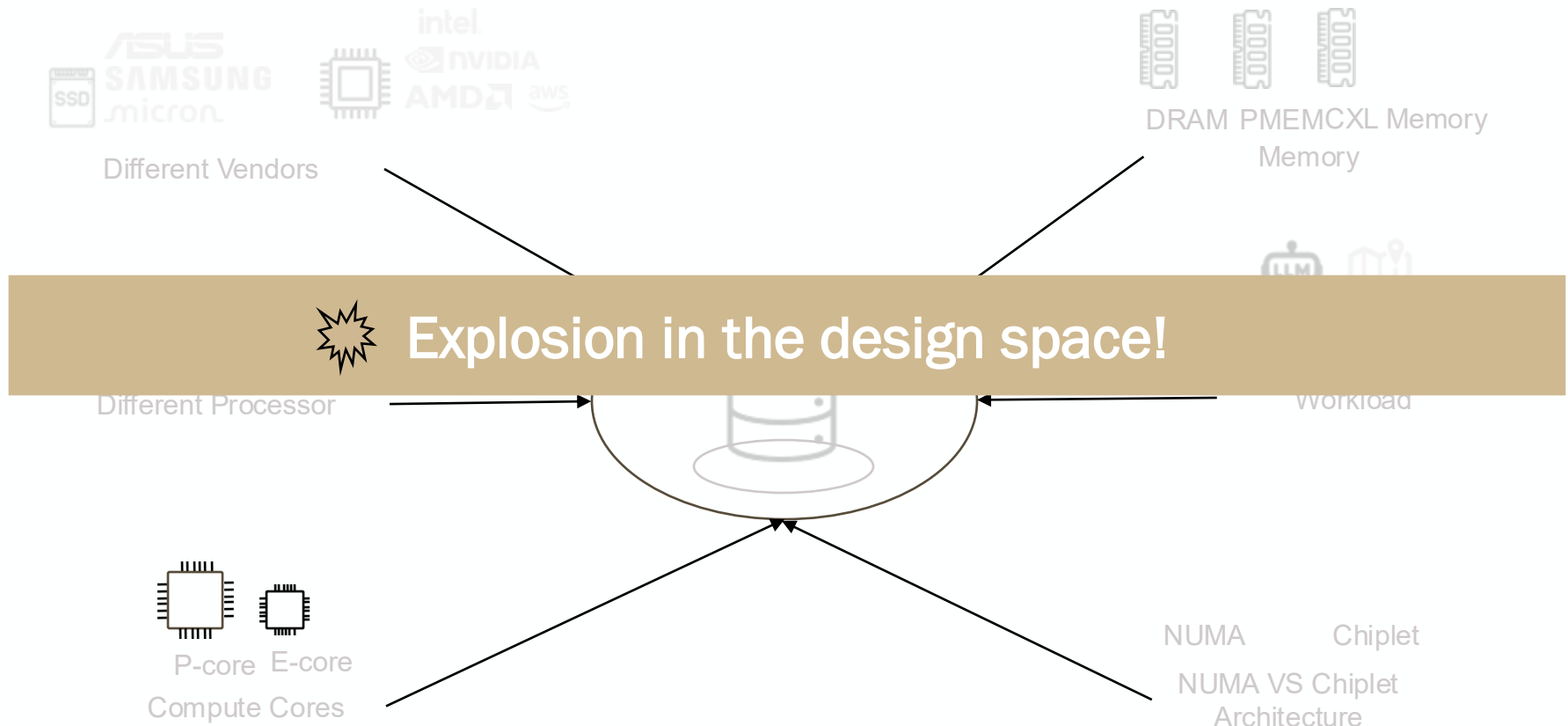


OLAP

Designing Databases for the Modern Era



Designing Databases for the Modern Era



What's Required?

Databases that are **generalizable** across heterogeneous hardware and workload without sacrificing performance.

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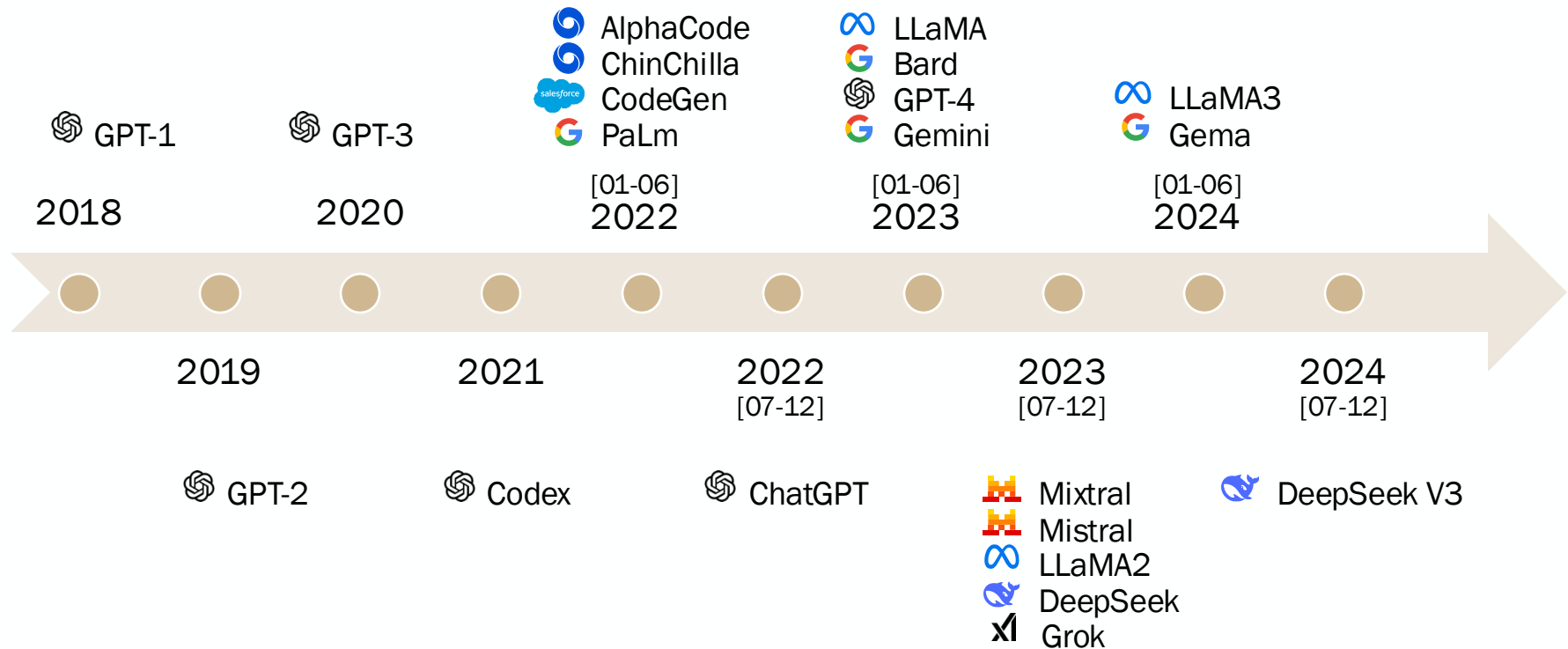
**Large Language Models
(LLM)**

Large Language Models (LLM)

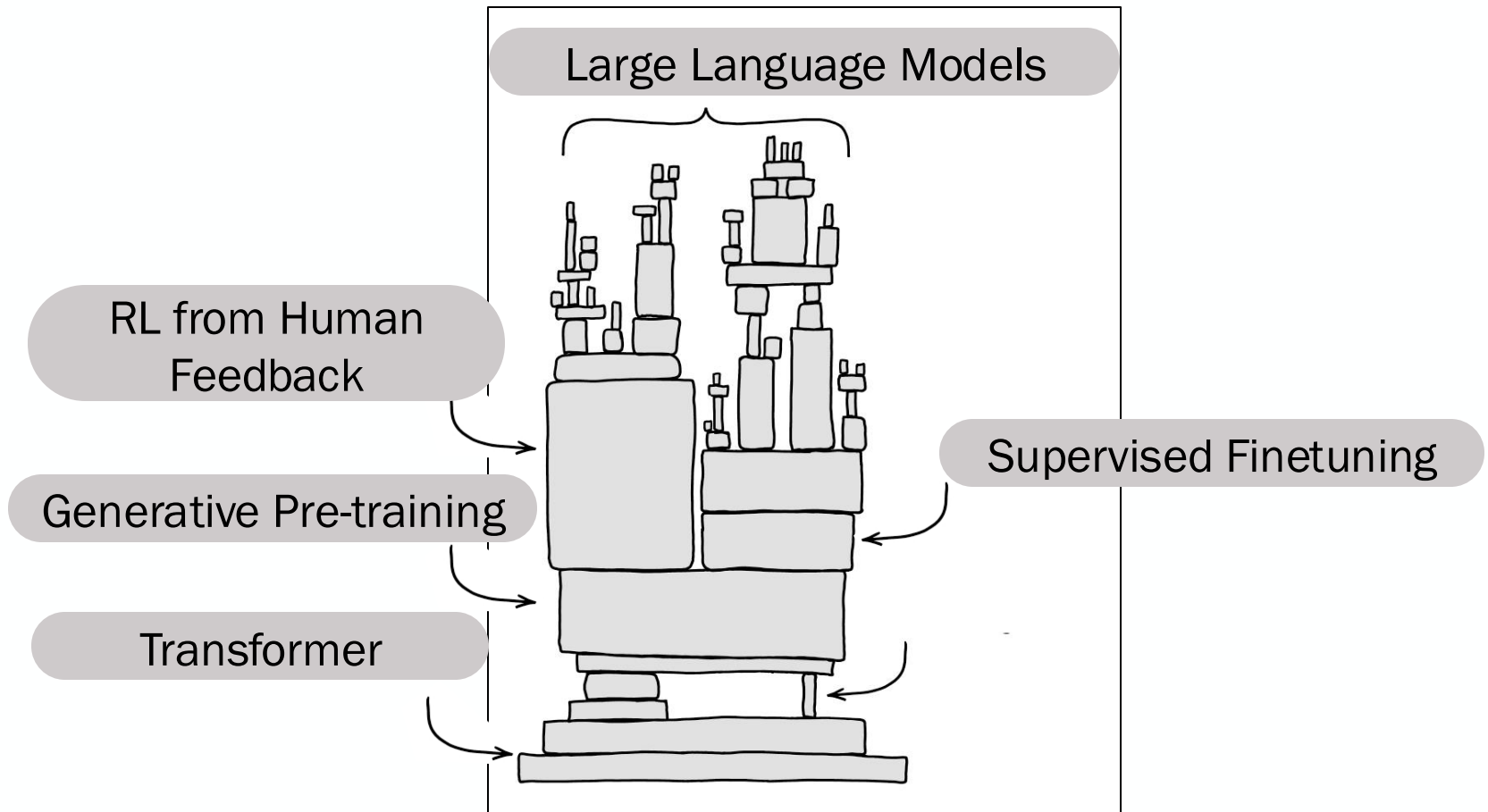
and

Next Token Prediction (NTP)

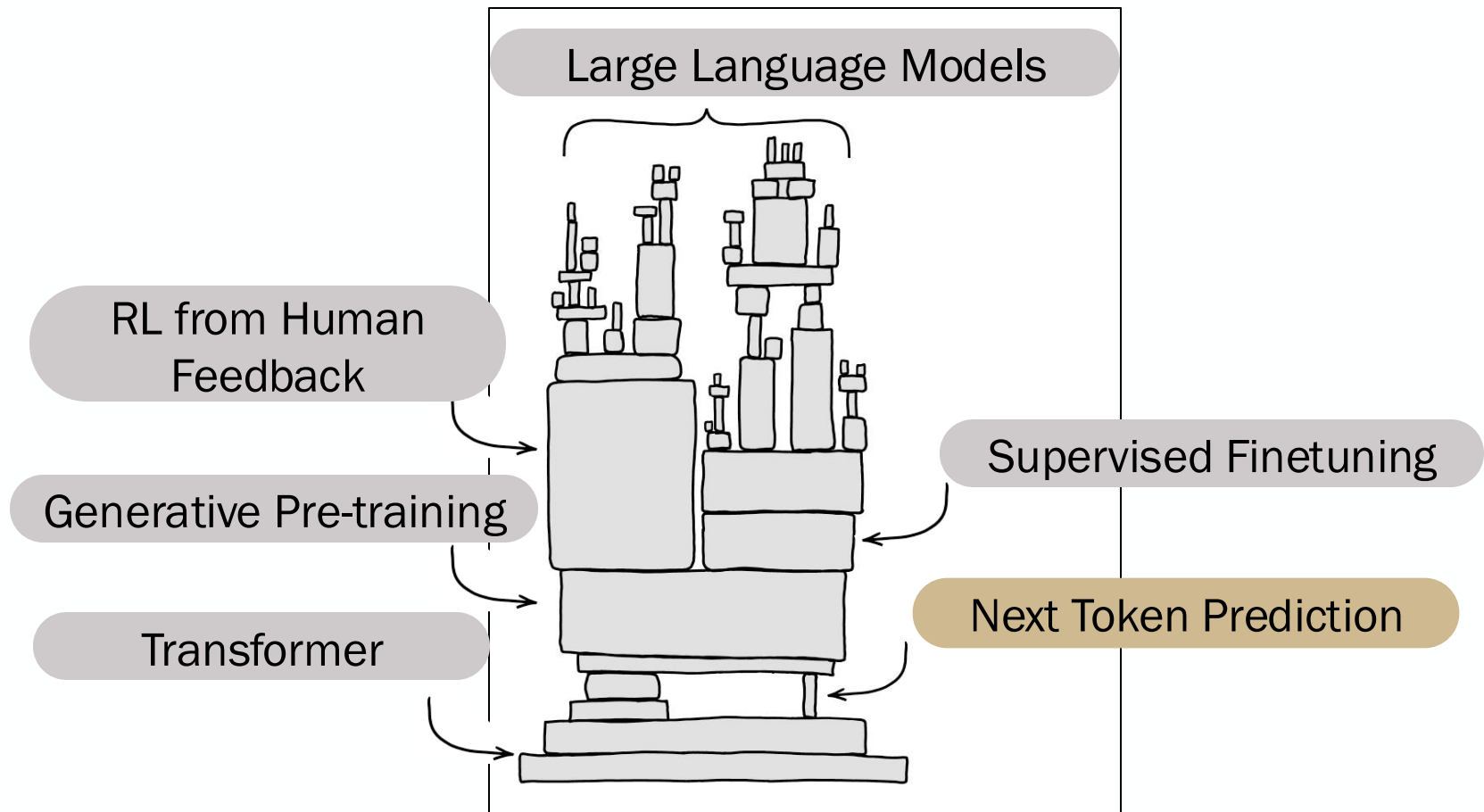
LLMs are everywhere!



The LLM Recipe



First Step Towards Adopting the LLM Recipe



What is Next Token Prediction?

- Next Token Prediction (NTP):
 - Model the probability of the next token in a given sequence based on the past tokens.
 - Token: a discrete unit, i.e., a word, a sub-word, or a character
- For any sequence τ
 - τ_i denotes the i -th token, and
 - $\tau_{<i}$ denotes the $i - 1$ tokens preceding τ_i
- Goal of NTP
 - Estimate the probability distribution of τ_i :
$$\mathbb{P}(\tau_i | \tau_{<i})$$

Generating a Sequence via NTP

SIGMOD is being 25 held Berlin in Germany

Vocabulary: Possible set of tokens

SIGMOD

First Token

Generating a Sequence via NTP

SIGMOD is being 25 held Berlin in Germany

Vocabulary: Possible set of tokens

SIGMOD

First Token



Next Token = ?

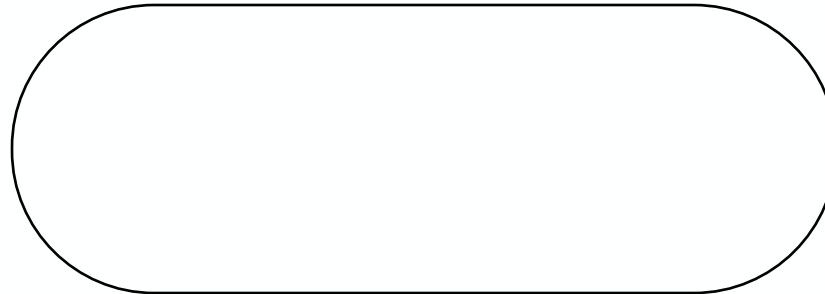
Generating a Sequence via NTP



Vocabulary: Possible set of tokens



First Token



Next Token = ?

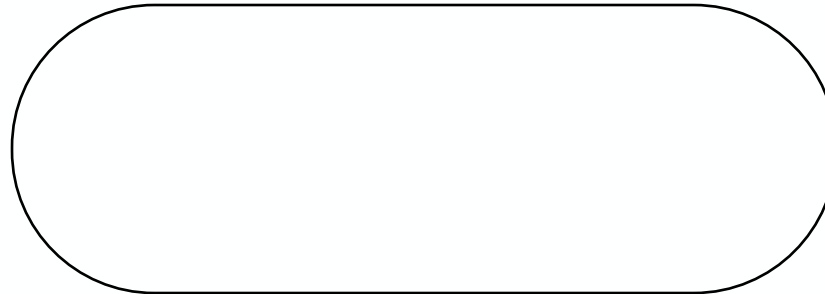
Generating a Sequence via NTP



Vocabulary: Possible set of tokens



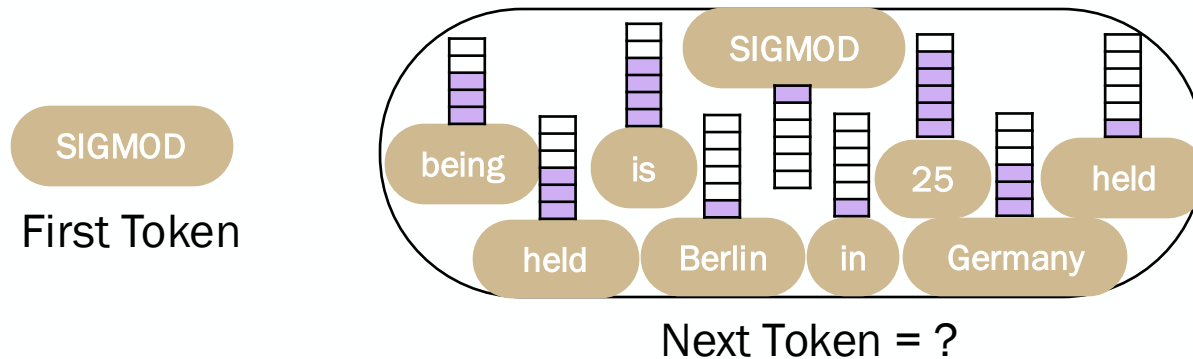
First Token



Next Token = ?

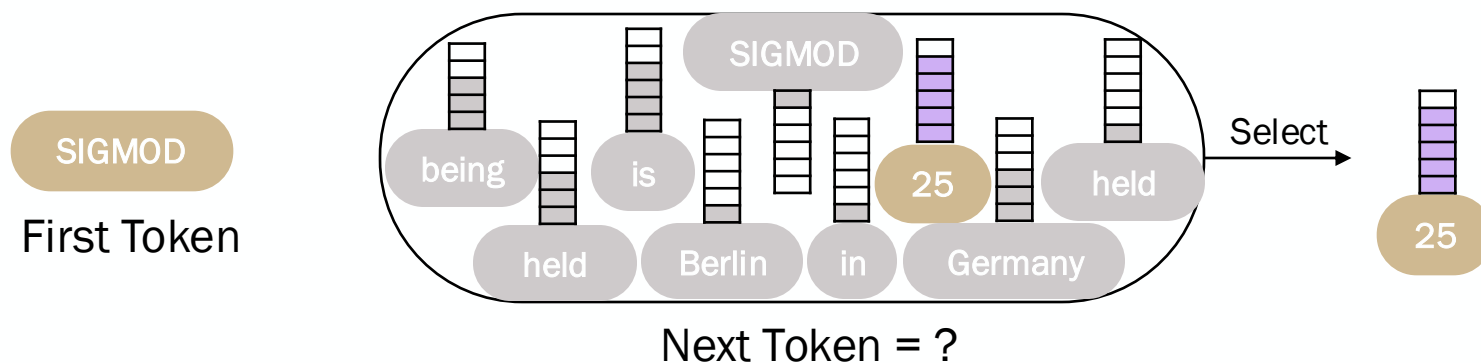
Generating a Sequence via NTP

- Estimate the probability distribution of each possible next tokens.



Generating a Sequence via NTP

- Select the token with the highest probability distribution as the next token.



Generating a Sequence via NTP



Vocabulary: Possible set of tokens

SIGMOD

First Token

25

Next Token

Generating a Sequence via NTP

SIGMOD is being 25 held Berlin in Germany

Vocabulary: Possible set of tokens

SIGMOD 25 is being held in Berlin Germany

Full Sentence

Next Token Prediction (NTP)

and

Database Systems

Translating NTP into Database Systems

- **Join Order Selection** ($A \bowtie B \bowtie C \bowtie D$)

- Tokens: The set of tables: A, B, C, D

Predicting the next table to join



- **Transaction scheduling** (T1, T2, T3, T4)

- Tokens: The set of transactions: T1, T2, T3, T4

Predicting the next transaction to schedule



Hindrances of Translating NLP into DBMS

1. The mismatch in objectives between the domain of NLP and Databases

NLP tasks	Database optimization tasks
Generative LLMs generate coherent sequences.	Goal-oriented
In Generative Pre-training phase, the goal is to compress a significant amount of world knowledge into the LLM by training on a diverse internet-scale corpus.	Improve query performance, scalability, and resource utilization.

Hindrances of Translating NTP into DBMS

2. The mismatch in the notion of Tokens in NLP and Databases

NLP tasks	Database optimization tasks
The notion of token is fixed for a particular tokenizer.	The notion of token is diverse and irregular. <ul style="list-style-type: none">• In JOS, tables are tokens.• In scheduling, transactions are tokens.

NLP Tokens VS Database Tokens

SIGMOD

25

is

being

held

in

Berlin

Germany

- Syntactic regularity:
 - The verb follows the subject.



SIGMOD

25

in

Germany

Berlin

held

is

being

- Contextual meaning:
 - The mention of Germany implicitly excludes other locations, e.g., USA.



SIGMOD

25

is

being

held

in

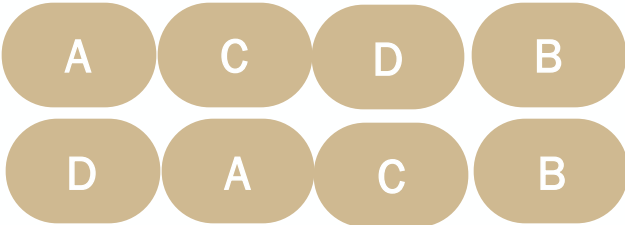
Chicago

USA

NLP Tokens VS Database Tokens



Join Order Selection ($A \bowtie B \bowtie C \bowtie D$)

- Lack of syntactic regularity:
 - Two different database instances can have tables with same name but with different attributes.
- Lack of contextual meaning:
 -  does not eliminate the possibility of

Contribution of this Paper

Database
Systems



Next Token
Prediction

Database Systems Meet Next Token Prediction

Building Blocks



DB-Tokens

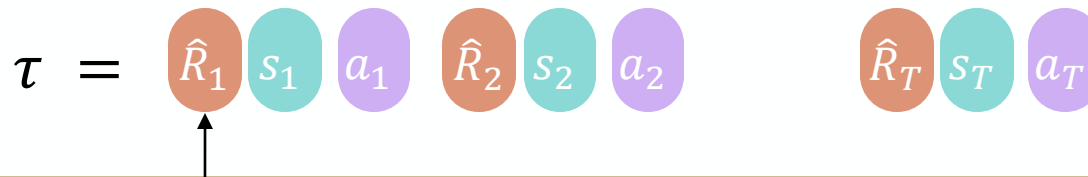


Decision Transformer

1. Decision Transformer: Sequence Modeling
2. DB-Tokens: Generalization

Building Block 1: Decision Transformer

- Goal-directed RL:
 - Treats RL as a supervised sequence modeling problem
 - Predict the next action (token) by conditioning on a desired reward, e.g., query throughput, latency, scalability etc.
- $$\mathbb{P}(a_i | s_i, \hat{R}_i, a_{<i}, s_{<i}, \hat{R}_{<i})$$
- Each trajectory (policy) is represented as a sequence of (return-to-go, state, action) tuples.

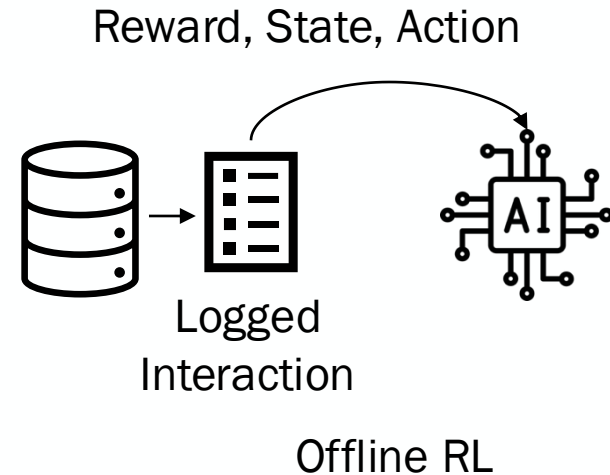
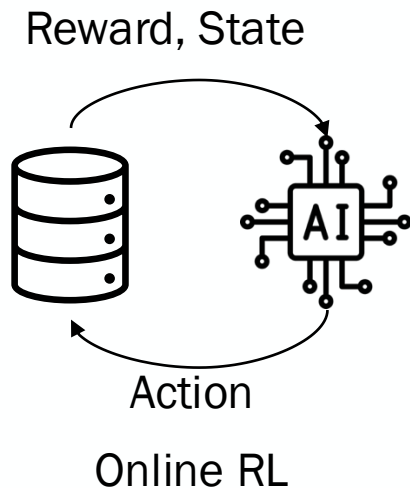


Reward-to-go token

Future cumulative reward expected from a given timestep onward.

Building Block 1: Decision Transformer

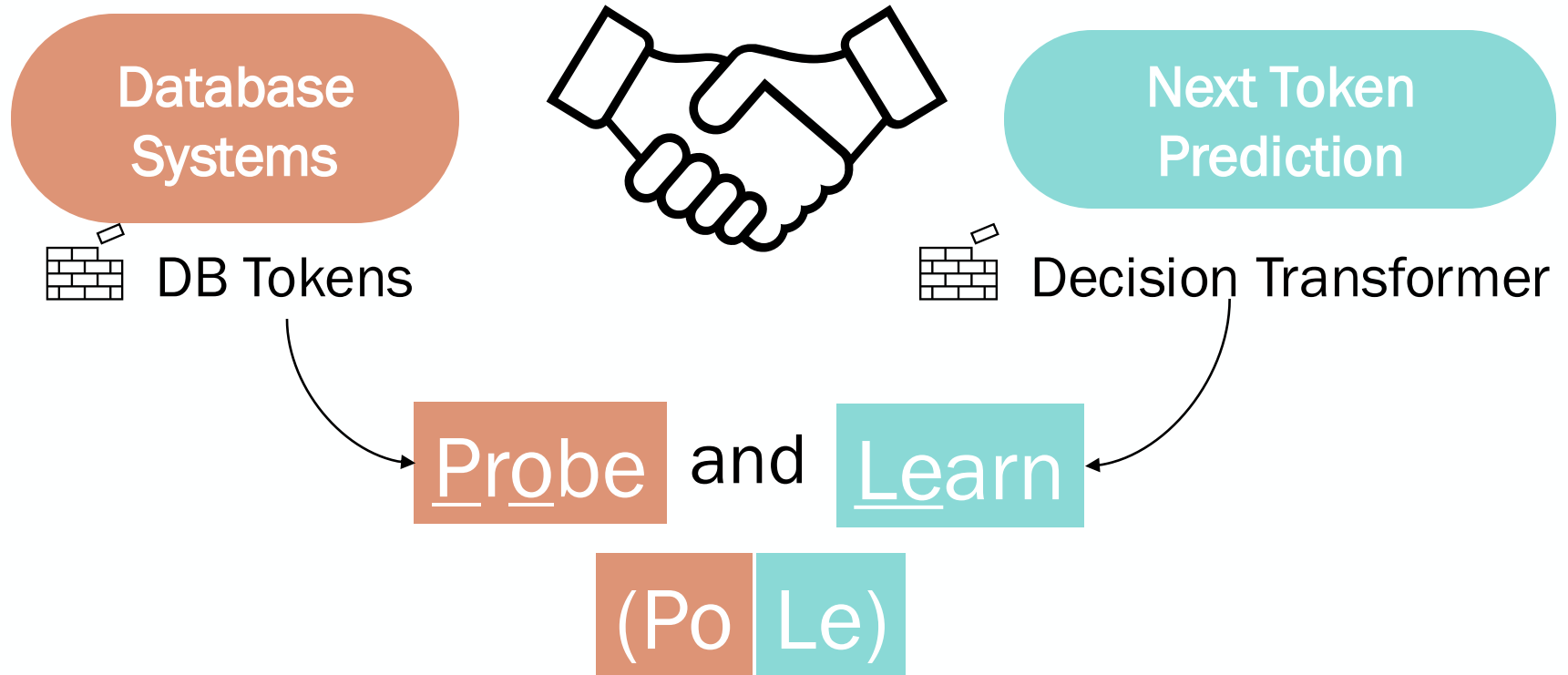
- Scalable across large datasets:
 - The underlying model is a Transformer architecture.
 - It follows the Offline Reinforcement Learning Paradigm
 - The model is trained on an offline dataset.
 - The model does not interact with the environment.



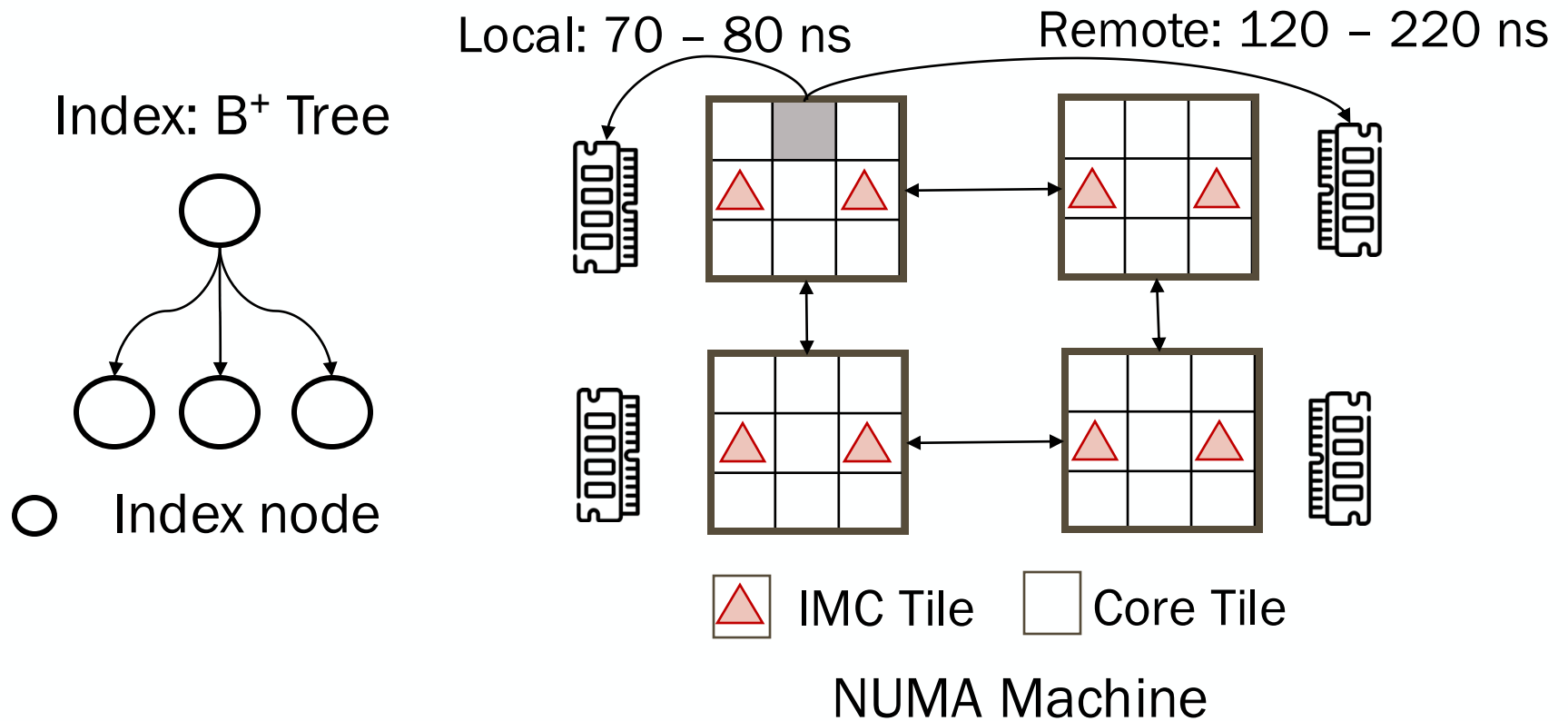
Building Block 2: DB-Tokens

- Hardware profiles generated from hardware Performance Monitoring Units (PMU, for short)
 - Computationally inexpensive to retrieve from the hardware registers
 - **Generalizable** across different hardware and workload applications
 - Can provide accurate hardware context that the DBMS is running on
 - Can mimic the data distribution and query workload

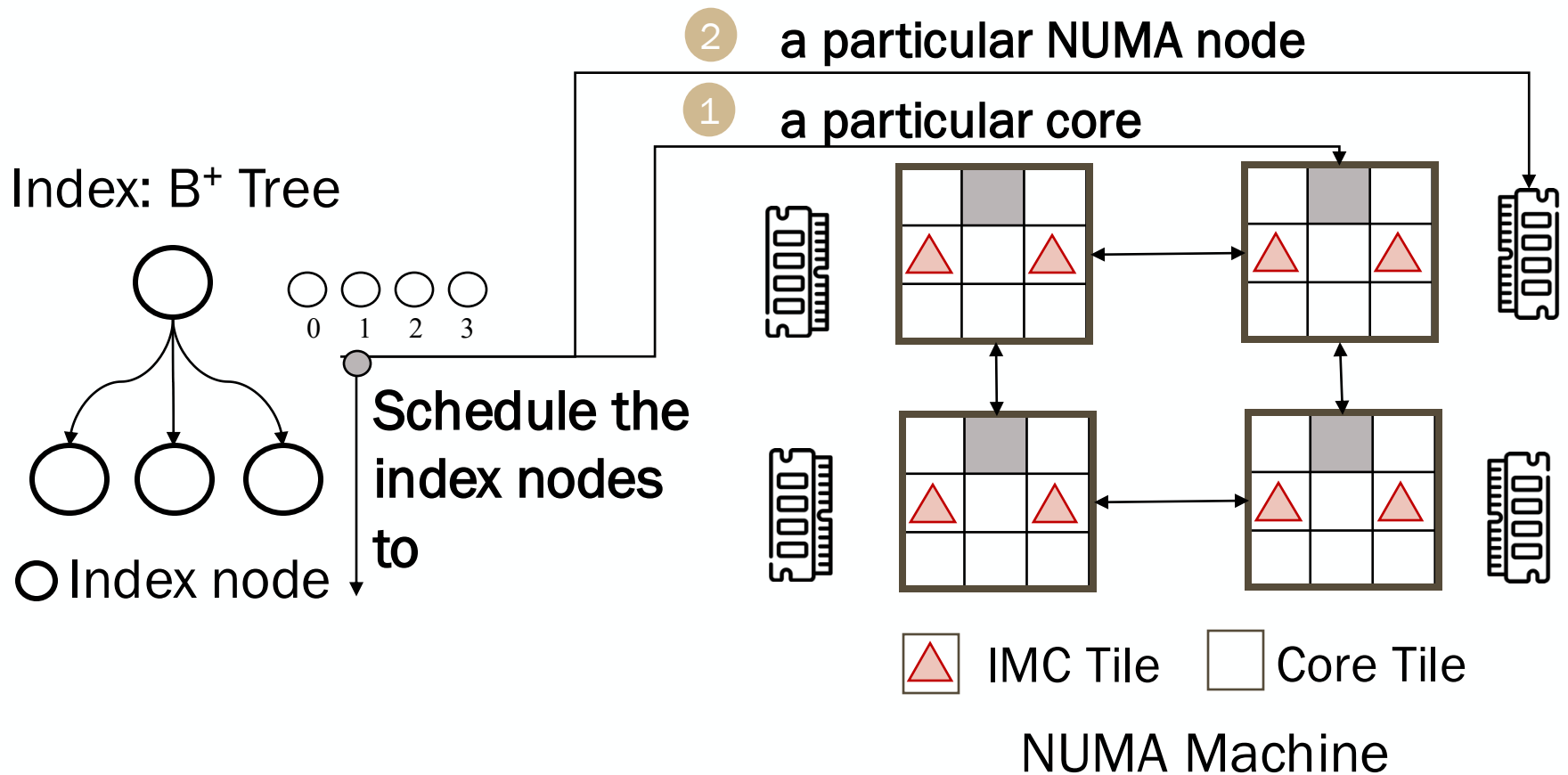
The Framework



Case Study: Index Scheduling

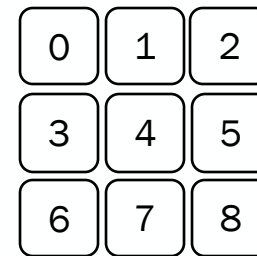
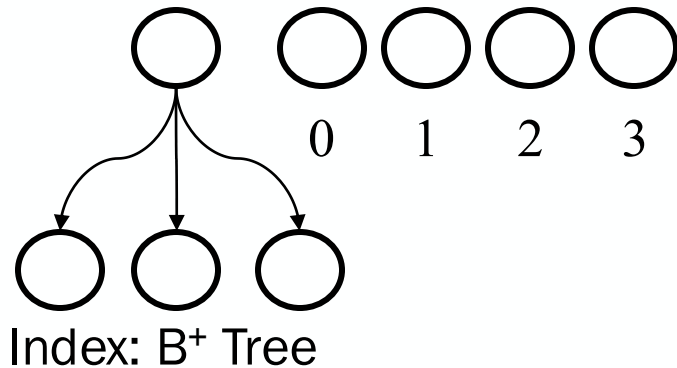


Case Study: Index Scheduling



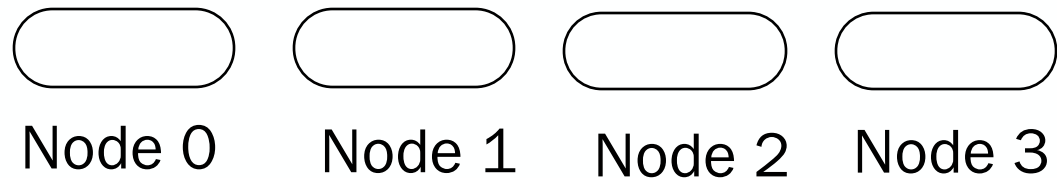
Index Scheduling: NTP Formulation

- Predict the next core to place the i -th index node
 - A policy in index scheduling = Sequence of core IDs



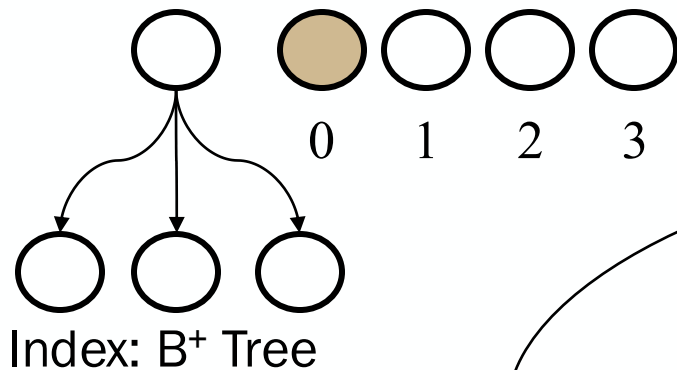
NUMA Machine

Predict the next
core to place

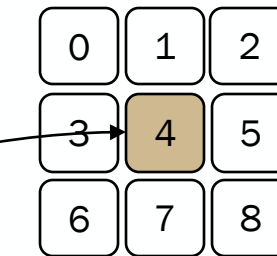


Index Scheduling: NTP Formulation

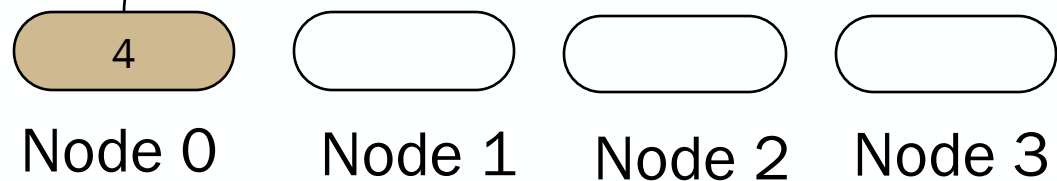
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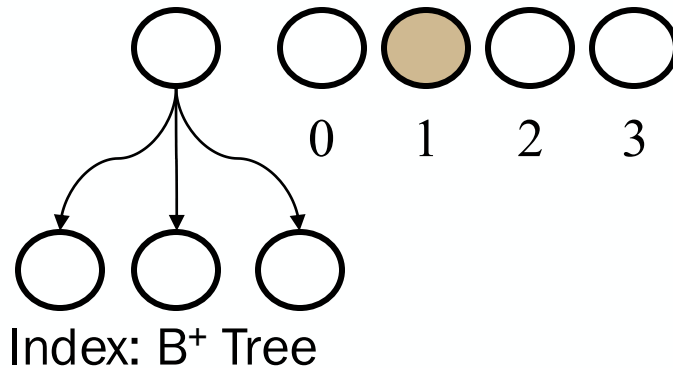


NUMA Machine



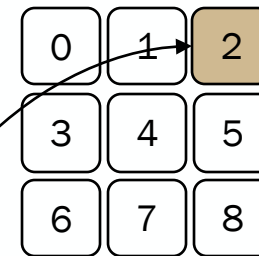
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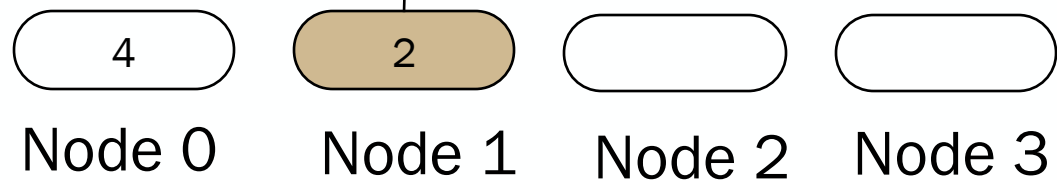


Index: B⁺ Tree

Predict the next
core to place

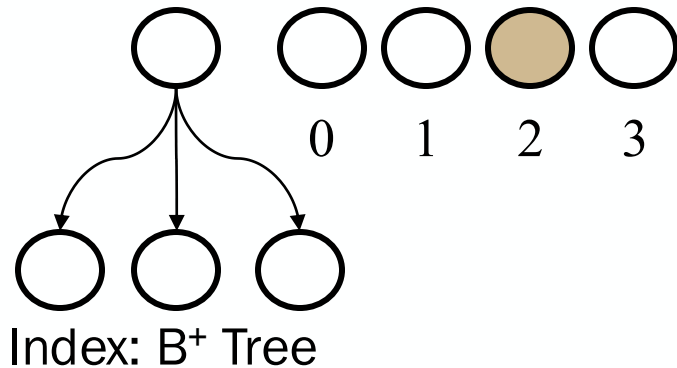


NUMA Machine

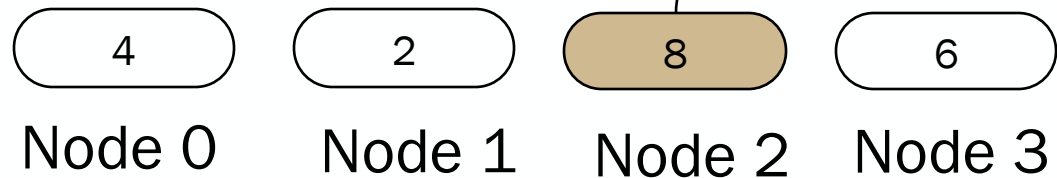


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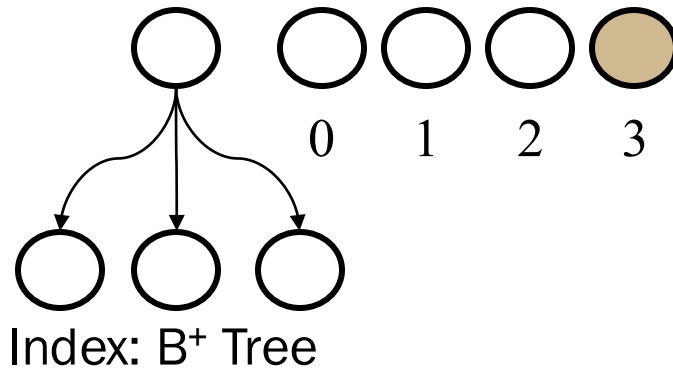


Predict the next
core to place

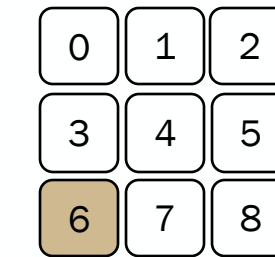


Index Scheduling: NTP Formulation

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Index: B⁺ Tree

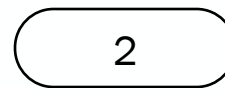


NUMA Machine

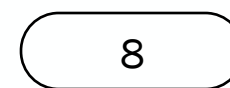
Predict the next
core to place



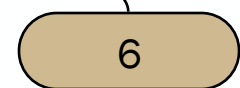
Node 0



Node 1



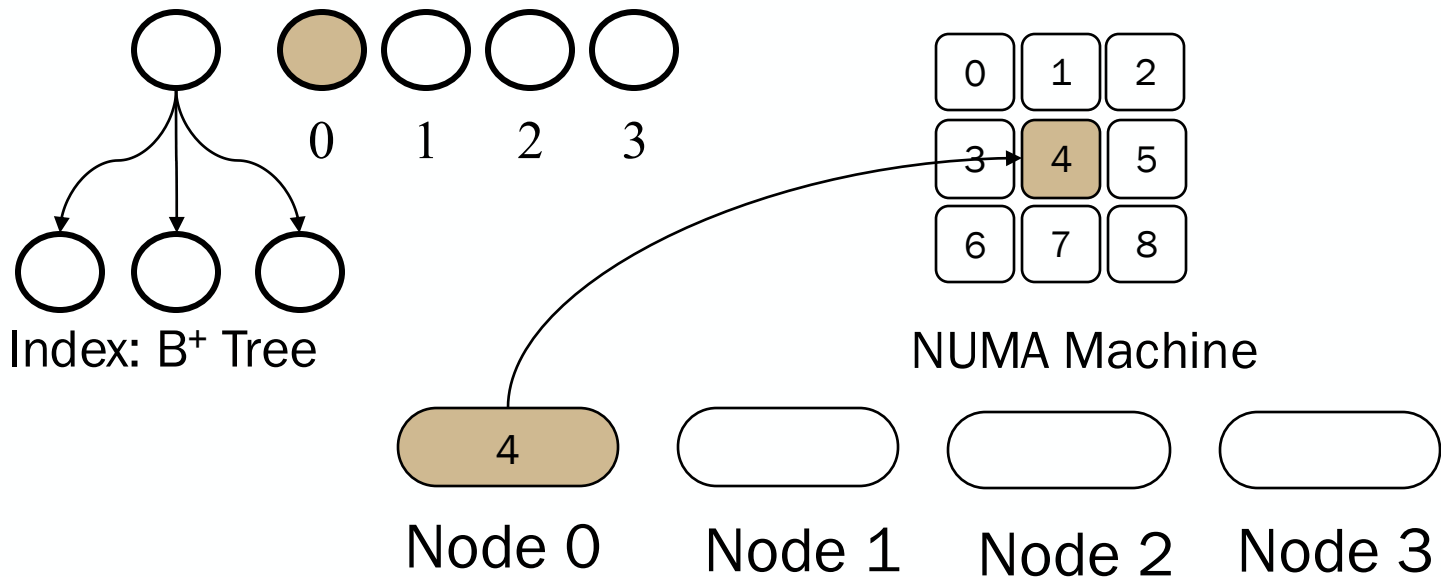
Node 2



Node 3

Index Scheduling: DB-Tokens

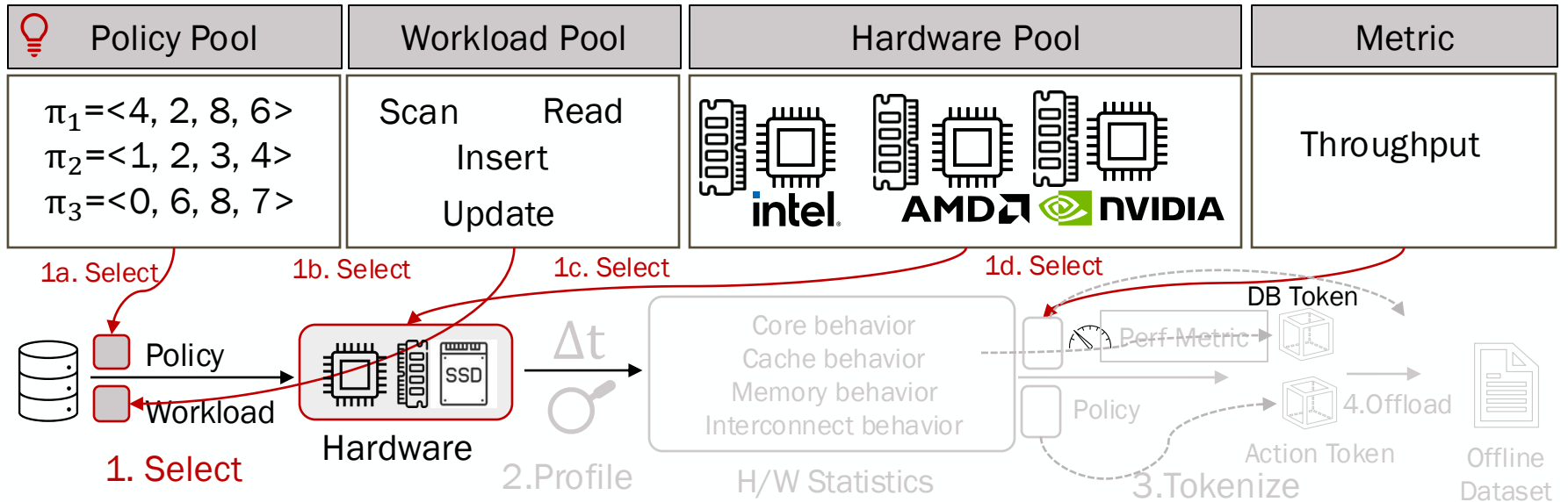
- Predict the next core to place the i -th index node
 - A policy in index scheduling = Sequence of core IDs



DB-Tokens: The L1, L2, LLC cache misses, branch misses, TLB misses, local and remote memory accesses of <Core 4>, query throughput of <Core 4>.

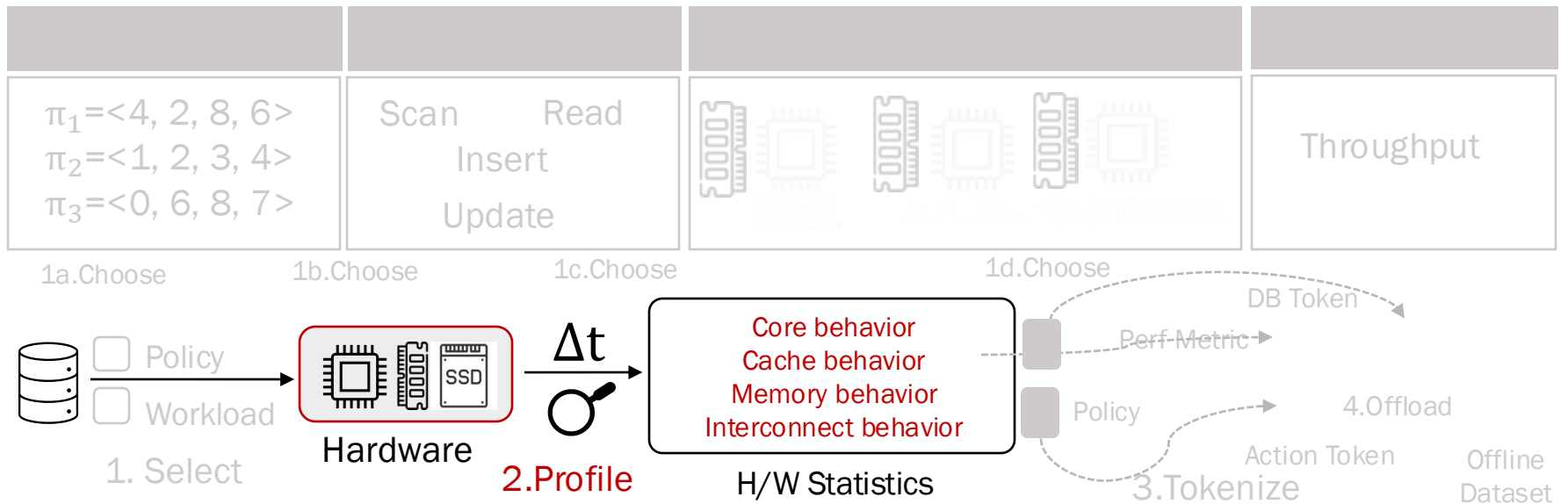
Probe Phase of PoLe Framework Index Scheduling

Probe Phase: Select (Step 1)



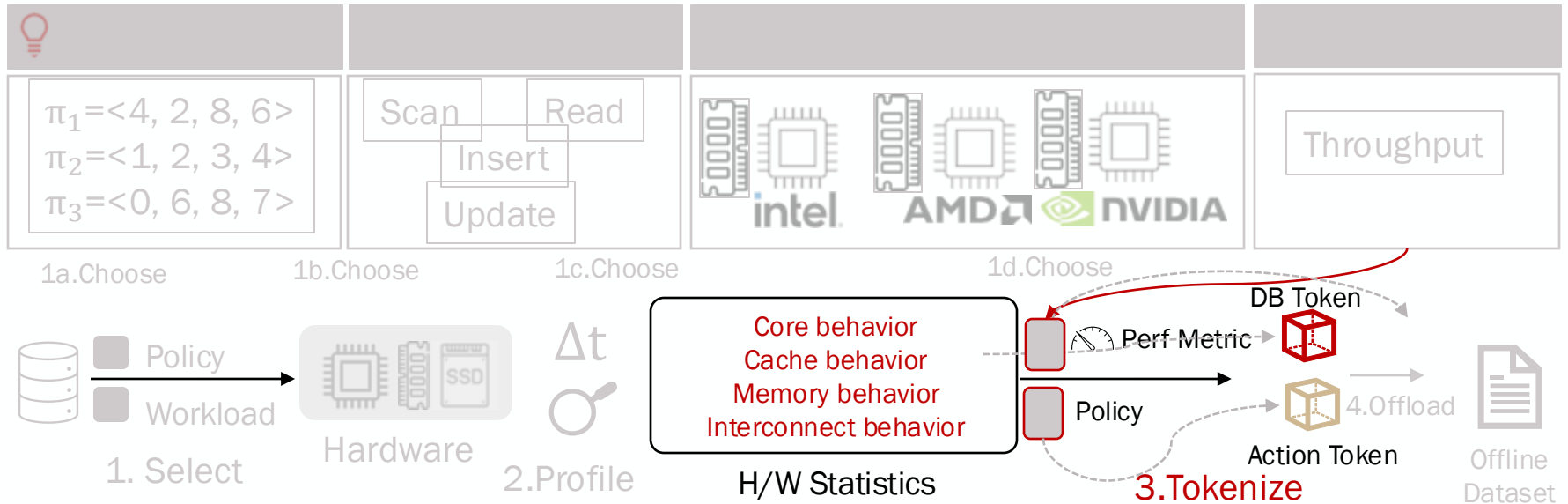
1. Select: Execute different policies across various hardware configurations and workloads, drawn from a diverse set of policy, hardware, and workload pools.

Probe Phase: Profile (Step 2)



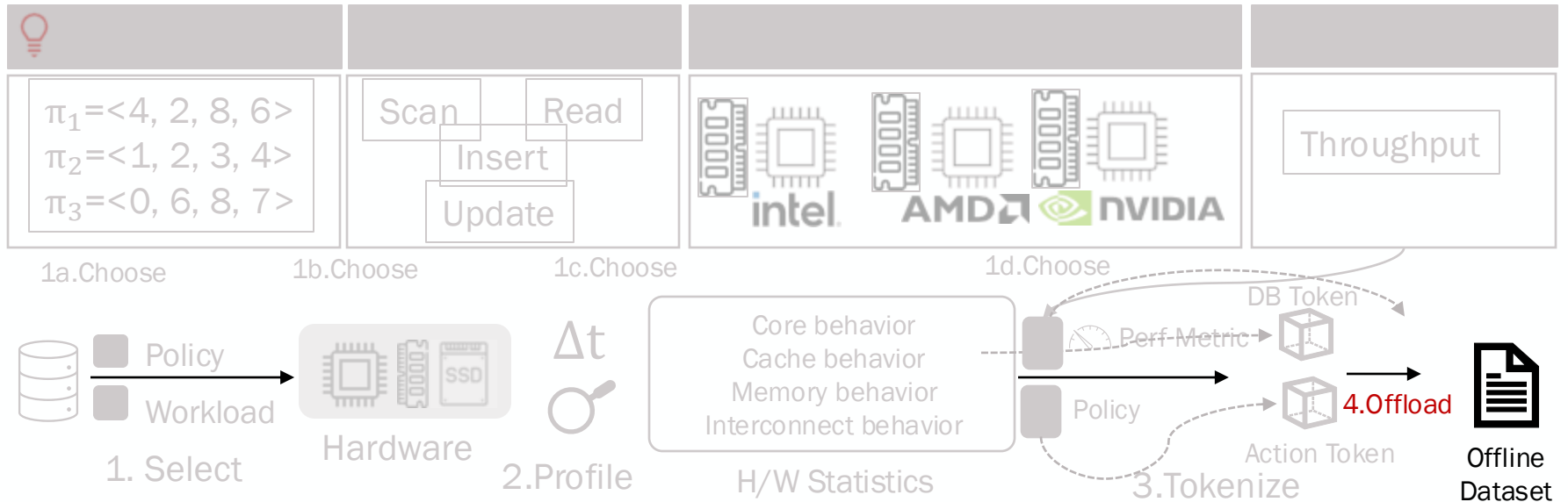
2. Profile: During query execution, periodically profile the hardware to capture the behavior of crucial hardware components.

Probe Phase: Tokenize (Step 3)



3. Tokenize: Tokenize the hardware profiles, alongside the desired performance metric and the current policy.

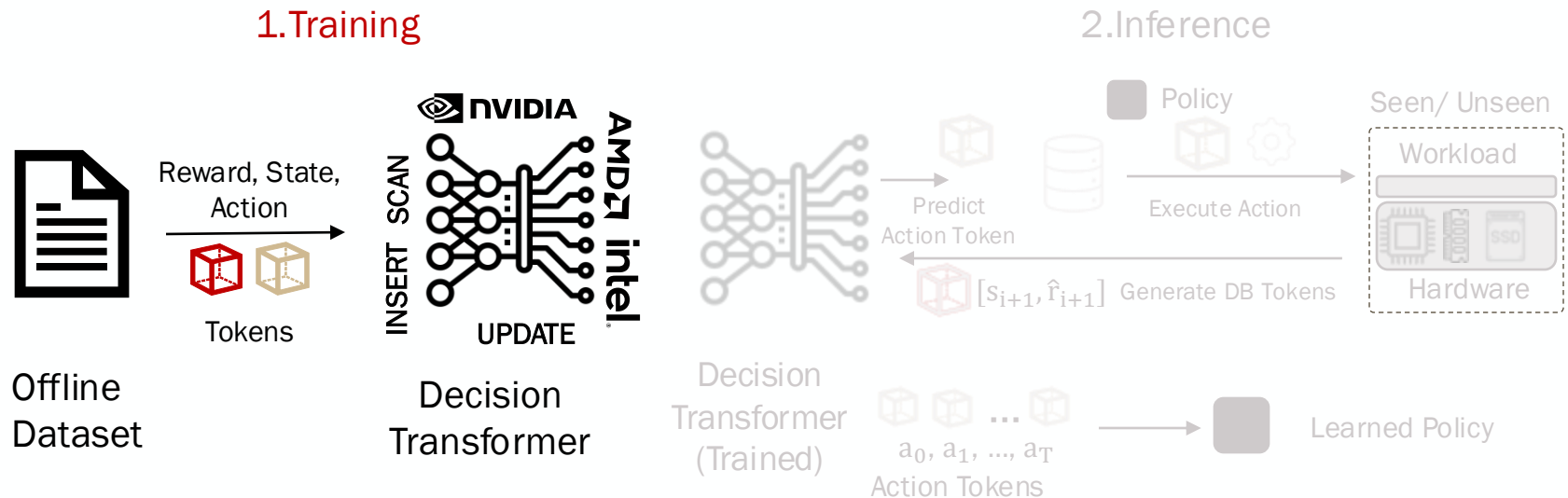
Probe Phase: Offload (Step 4)



4. Offload: Offload the action tokens along with the associated DB-tokens to an offline dataset. This is provided to the Decision Transformer during the learning phase.

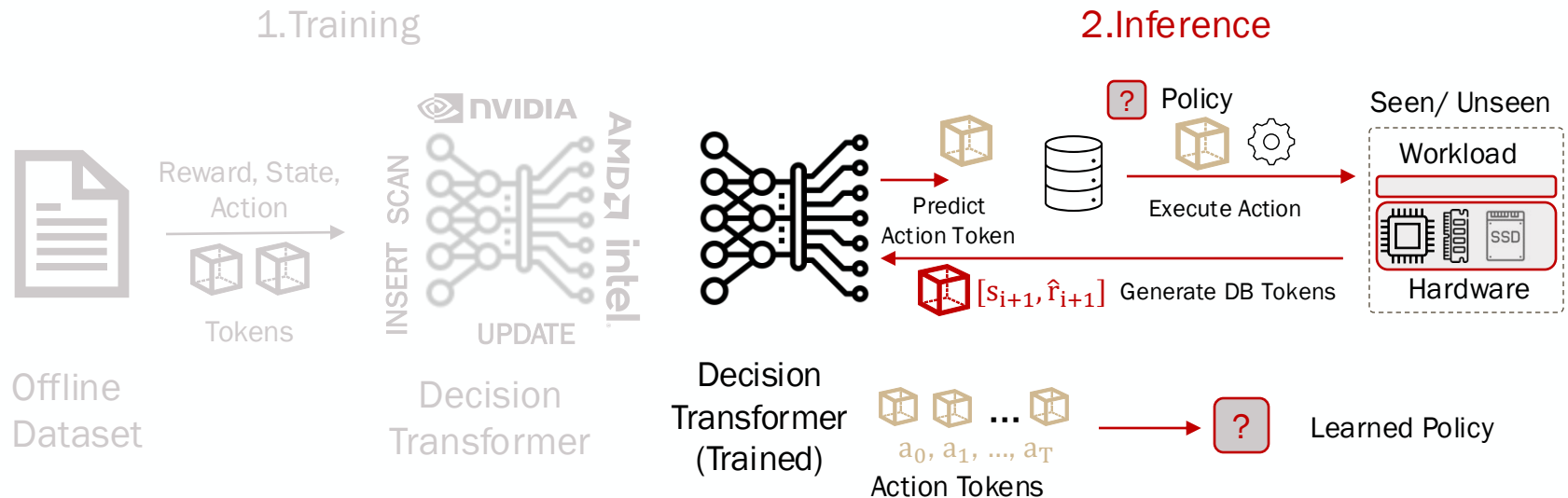
Learn Phase of PoLe Framework Index Scheduling

Learn Phase: Training (Step 1)



1. **Training:** Train a Decision Transformer (DT) on the collected offline dataset in a supervised manner.

Learn Phase: Inference (Step 2)



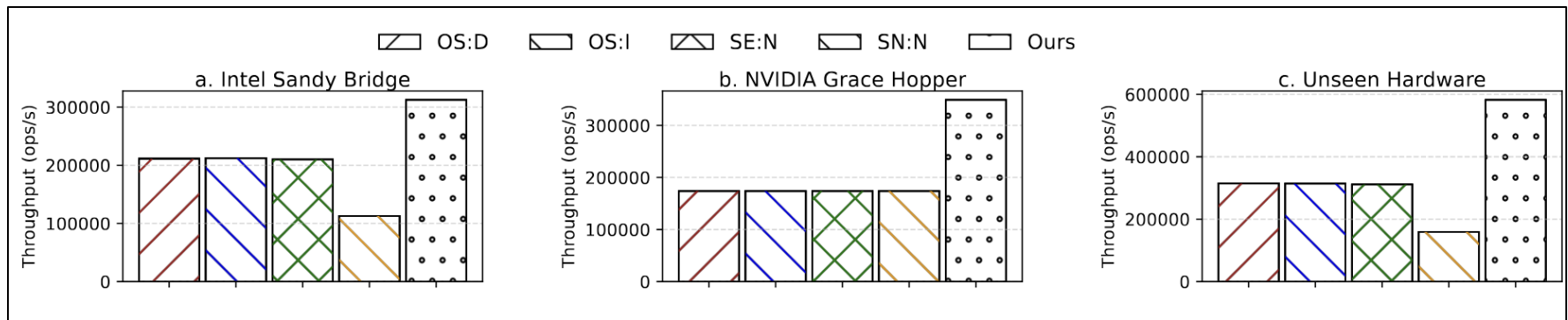
2. Inference: Infer a new policy via auto-regression using the trained Decision Transformer.

Experiment Settings

- Index: A main-memory B^+ -Tree
- Workload: YCSB-A workload
- Baselines
 - OS Scheduling Policies
 - Default, Local, Interleaved
 - OS handles core scheduling.
 - Heuristics:
 - Shared Everything NUMA
 - Nearby index nodes are placed on the same NUMA node.
 - OS handles scheduling.
 - Shared Nothing
 - Nearby index nodes are placed on the same NUMA node.
 - Core Scheduling follows the data placement strategy.

Preliminary Results

- On seen hardware (Intel Sandy Bridge and NVIDIA Grace Hopper)
 - PoLe outperforms the baselines by up to 2.78X
- On unseen hardware (Intel Skylake X)
 - PoLe outperforms the baselines by up to 3X



PoLe's Learned Scheduling Policies

Intel Sandy Bridge (π_l)

32	28	24	44	16	28	48	48	29	29	29	37	25	49	45	51
45	41	21	25	29	51	51	13	33	42	34	45	30	34	27	41
34	45	30	34	29	34	34	30	30	13	36	12	12	12	12	14
26	13	35	20	34	43	39	39	28	26	26	33	26	33	35	50
16	16	16	16	14	18	38	38	47	47	47	50	39	16	21	47
47	28	39	47	47	12	39	39	28	12	28	41	12	16	28	41
31	31	31	31	48	22	13	13	13	23	23	29	29	31	31	31
33	21	45	45	33	44	14	46	46	30	36	36	45	36	17	17
21	43	21	44	51	19	19	15	26	19	29	15	49	49	40	42
30	30	49	49	13	35	17	17	17	39	39	29	29	26	30	29
29	14	29	42	41	26	41	37	37	37	37	30	30	39	13	15
15	39	26	13	17	46	17	34	13	13	13	35	26	31	31	22
22	22	46	46	42	33	33	46	46	46	37	33	33	33	46	37
33	33	37	39	39	22	24	39	21	13	26	26	17	24	24	30
30	16	24	19	19	43	24	24	43	31	43	15	21	21	21	21
50	16	15	31	31	38	43	38	38	38	31	50	38	50	38	38

NVIDIA Grace Hopper (π_l)

13	16	57	52	25	16	27	35	8	17	6	38	40	13	36	4
5	26	56	32	34	43	31	47	6	29	29	15	58	3	46	14
19	48	55	35	53	34	33	39	39	42	39	39	14	53	3	19
45	35	11	49	42	50	55	15	15	15	15	58	53	42	7	19
7	44	42	42	21	54	41	41	41	9	44	21	21	21	21	45
9	9	45	35	51	35	43	43	51	8	39	53	31	31	31	41
20	31	57	46	46	30	55	55	55	30	30	30	36	36	37	36
37	46	37	46	4	37	37	4	4	4	36	58	58	52	43	58
52	41	40	52	24	27	24	40	34	24	52	27	24	24	40	34
56	27	43	27	5	40	19	19	9	34	54	10	56	18	5	44
49	38	6	10	30	29	29	47	53	56	50	50	50	50	57	38
48	48	56	38	54	38	48	47	54	48	57	47	57	23	29	10
10	10	54	18	6	6	44	18	28	25	20	5	25	20	28	45
28	28	25	25	7	28	5	20	20	45	11	11	13	7	44	23
7	13	18	13	11	18	11	23	23	23	47	49	9	12	51	22
26	12	3	8	49	3	12	8	3	22	22	22	17	22	12	49

Intel Skylake X (π_l)

31	4	9	7	4	8	7	9	3	3	8	7	6	8	6	5
3	9	6	4	4	3	7	9	52	8	6	4	3	9	6	4
3	4	4	3	9	7	9	30	53	30	6	7	7	6	5	81
80	30	30	30	27	79	6	27	27	27	8	53	8	8	53	28
30	30	54	52	52	52	54	30	8	54	76	8	28	28	33	55
3	28	33	75	8	75	3	3	76	29	29	6	6	52	52	52
52	32	80	32	29	29	9	80	77	9	80	80	5	53	9	80
76	78	5	28	5	32	30	32	31	31	31	51	32	78	51	51
51	31	51	51	51	51	78	76	51	51	32	32	76	32	32	33
33	33	32	78	29	31	80	31	31	33	33	31	80	33	31	33
33	27	77	53	80	27	53	29	80	53	53	29	53	29	28	54
4	28	52	53	52	27	27	27	28	27	30	54	54	79	29	29
4	28	28	79	54	78	54	54	78	54	76	78	76	55	78	76
76	55	55	76	81	81	81	55	5	55	77	5	78	78	5	77
5	77	5	77	81	81	75	75	81	81	75	81	75	81	77	75
56	75	75	79	56	75	79	79	57	57	56	79	57	79	79	57

Cells sharing the same color indicate that the associated index chunks are scheduled on the same NUMA server.

PoLe's Learned Scheduling Policies

Intel Sandy Bridge (π_l)

32	28	24	44	16	28	48	48	29	29	29	37	25	49	45	51
45	41	21	25	29	51	51	13	33	42	34	45	30	34	27	41
34	45	30	34	29	34	34	30	30	13	36	12	12	12	12	14
26	13	35	20	34	43	39	39	28	26	26	33	26	33	35	50
16	16	16	16	14	18	38	38	47	47	47	50	39	16	21	47
47	28	39	47	47	12	39	39	28	12	28	41	12	16	28	41
31	31	31	31	48	22	13	13	13	23	23	29	29	31	31	31
33	21	45	45	33	44	14	46	46	30	36	36	45	36	17	17
21	43	21	44	51	19	19	15	26	19	29	15	49	49	40	42
30	30	49	49	13	35	17	17	17	39	39	29	29	26	30	29
29	14	29	42	41	26	41	37	37	37	37	30	30	39	13	15
15	39	26	13	17	46	17	34	13	13	13	35	26	31	31	22
22	22	46	46	42	33	33	46	46	46	37	33	33	33	46	37
33	33	37	39	39	22	24	39	21	13	26	26	17	24	24	30
30	16	24	19	19	43	24	24	43	31	43	15	21	21	21	21
50	16	15	31	31	38	43	38	38	38	31	50	38	50	38	38

32

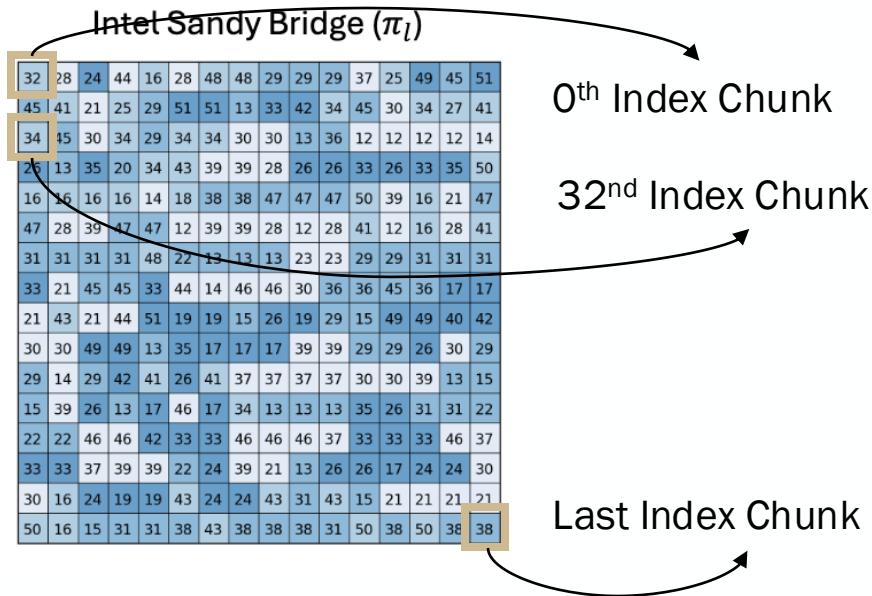
Index Chunk (A collection of index nodes)

Core ID
where the chunk is scheduled

Scheduling Policy

Cells sharing the same color indicate that the associated index chunks are scheduled on the same NUMA server.

PoLe's Learned Scheduling Policies



Cells sharing the same color indicate that the associated index chunks are scheduled on the same NUMA server.

PoLe's Scheduling Policies

PoLe's Learned Scheduling Policies

Intel Sandy Bridge (π_I)

32	28	24	44	16	28	48	48	29	29	29	37	25	49	45	51
45	41	21	25	29	51	51	13	33	42	34	45	30	34	27	41
34	45	30	34	29	34	34	30	30	13	36	12	12	12	12	14
26	13	35	20	34	43	39	39	28	26	26	33	26	33	35	50
16	16	16	16	14	18	38	38	47	47	47	50	39	16	21	47
47	28	39	47	47	12	39	39	28	12	28	41	12	16	28	41
31	31	31	31	48	22	13	13	13	23	23	29	29	31	31	31
33	21	45	45	33	44	14	46	46	30	36	36	45	36	17	17
21	43	21	44	51	19	19	15	26	19	29	15	49	49	40	42
30	30	49	49	13	35	17	17	17	39	39	29	29	26	30	29
29	14	29	42	41	26	41	37	37	37	37	30	30	39	13	15
15	39	26	13	17	46	17	34	13	13	13	35	26	31	31	22
22	22	46	46	42	33	33	46	46	46	37	33	33	33	46	37
33	33	37	39	39	22	24	39	21	13	26	26	17	24	24	30
30	16	24	19	19	43	24	24	43	31	43	15	21	21	21	21
50	16	15	31	31	38	43	38	38	31	50	38	50	38	38	38

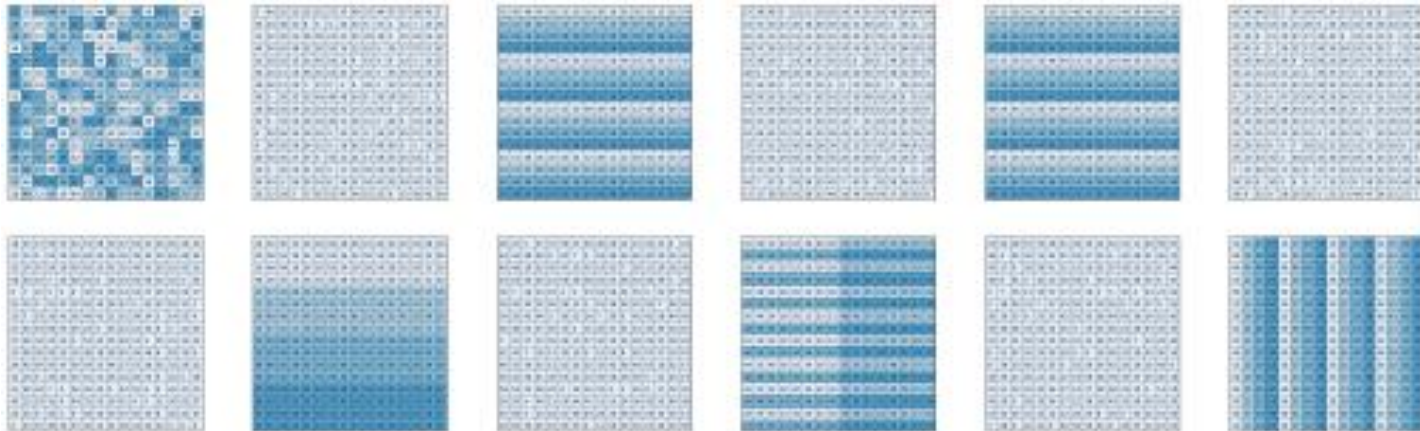
NVIDIA Grace Hopper (π_I)

13	16	57	52	25	16	27	35	8	17	6	38	40	13	36	4
5	26	56	32	34	43	31	47	6	29	29	15	58	3	46	14
19	48	55	35	53	34	33	39	39	42	39	39	14	53	3	19
45	35	11	49	42	50	55	15	15	15	58	53	42	7	19	
7	44	42	42	21	54	41	41	41	9	44	21	21	21	21	45
9	9	45	35	51	35	43	43	51	8	39	53	31	31	31	41
20	31	57	46	46	30	55	55	55	30	30	30	36	36	37	36
37	46	37	46	4	37	37	4	4	4	36	58	58	52	43	58
52	41	40	52	24	27	24	40	34	24	52	27	24	24	40	34
56	27	43	27	5	40	19	19	9	34	54	10	56	18	5	44
49	38	6	10	30	29	29	47	53	56	50	50	50	50	57	38
48	48	56	38	54	38	48	47	54	48	57	47	57	23	29	10
10	10	54	18	6	6	44	18	28	25	20	5	25	20	28	45
28	28	25	25	7	28	5	20	20	45	11	11	13	7	44	23
7	13	18	13	11	18	11	23	23	23	47	49	9	12	51	22
26	12	3	8	49	3	12	8	3	22	22	22	17	22	12	49

Intel Skylake X (π_I)

31	4	9	7	4	8	7	9	3	3	8	7	6	8	6	5
3	9	6	4	4	3	7	9	52	8	6	4	3	9	6	4
3	4	4	3	9	7	9	30	53	30	6	7	7	6	5	81
80	30	30	30	27	79	6	27	27	27	8	53	8	8	53	28
30	30	54	52	52	52	54	30	8	54	76	8	28	28	33	55
3	28	33	75	8	75	3	3	76	29	29	6	6	52	52	52
52	32	80	32	29	29	9	80	77	9	80	80	5	53	9	80
76	78	5	28	5	32	30	32	31	31	31	51	32	78	51	51
51	31	51	51	51	51	78	76	51	51	32	32	76	32	32	33
33	33	32	78	29	31	80	31	31	33	33	31	80	33	31	33
33	27	77	53	80	27	53	29	80	53	53	29	53	29	28	54
4	28	52	53	52	27	27	27	28	27	30	54	54	79	29	29
4	28	28	79	54	78	54	54	78	54	76	78	76	55	78	76
76	55	55	76	81	81	81	55	5	55	77	5	78	78	5	77
5	77	5	77	81	81	75	75	81	81	75	81	75	81	77	75
56	75	75	79	56	75	79	79	57	57	56	79	57	79	79	57

Scheduling Policies in the training set



PoLe's Learned Scheduling Policies

PoLe's Learned Scheduling Policies

Intel Sandy Bridge (π_I)

32	28	24	44	16	28	48	48	29	29	29	37	25	49	45	51
45	41	21	25	29	51	51	13	33	42	34	45	30	34	27	41
34	45	30	34	29	34	34	30	13	36	12	12	12	12	14	
26	13	35	20	34	43	39	39	28	26	26	33	26	33	35	50
16	16	16	16	14	18	38	38	47	47	47	50	39	16	21	47
47	28	39	47	47	12	39	39	28	12	28	41	12	16	28	41
31	31	31	31	48	22	13	13	13	23	23	29	29	31	31	31
33	21	45	45	33	44	14	46	46	30	36	36	45	36	17	17
21	43	21	44	51	19	19	15	26	19	29	15	49	49	40	42
30	30	49	49	13	35	17	17	17	39	39	29	29	26	30	29
29	14	29	42	41	26	41	37	37	37	30	30	30	39	13	15
15	39	26	13	17	46	17	34	13	13	13	35	26	31	31	22
22	22	46	46	42	33	33	46	46	46	37	33	33	33	46	37
33	33	37	39	39	22	24	39	21	13	26	26	17	24	24	30
30	16	24	19	19	43	24	24	43	31	43	15	21	21	21	21
50	16	15	31	31	38	43	38	38	38	31	50	38	50	38	38

NVIDIA Grace Hopper (π_I)

13	16	57	52	25	16	27	35	8	17	6	38	40	13	36	4
5	26	56	32	34	43	31	47	6	29	29	15	58	3	46	14
19	48	55	35	53	34	33	39	39	42	39	39	14	53	3	19
45	35	11	49	42	50	55	15	15	15	58	53	42	7	19	
7	44	42	42	21	54	41	41	9	44	21	21	21	21	45	
9	9	45	35	51	35	43	43	51	8	39	53	31	31	31	41
20	31	57	46	46	30	55	55	55	30	30	30	36	36	37	36
37	46	37	46	4	37	37	4	4	4	36	58	58	52	43	58
52	41	40	52	24	27	24	40	34	24	52	27	24	24	40	34
56	27	43	27	5	40	19	19	9	34	54	10	56	18	5	44
49	38	6	10	30	29	29	47	53	56	50	50	50	50	57	38
48	48	56	38	54	38	48	47	54	48	57	47	57	23	29	10
10	10	54	18	6	6	44	18	28	25	20	5	25	20	28	45
28	28	25	25	7	28	5	20	20	45	11	11	13	7	44	23
7	13	18	13	11	18	11	23	23	23	47	49	9	12	51	22
26	12	3	8	49	3	12	8	3	22	22	22	17	22	12	49

Intel Skylake X (π_I)

31	4	9	7	4	8	7	9	3	3	8	7	6	8	6	5
3	9	6	4	4	3	7	9	52	8	6	4	3	9	6	4
3	4	4	3	9	7	9	30	53	30	6	7	7	6	5	81
80	30	30	30	27	79	6	27	27	27	8	53	8	8	53	28
30	30	54	52	52	52	54	30	8	54	76	8	28	28	33	55
3	28	33	75	8	75	3	3	76	29	29	6	6	52	52	52
52	32	80	32	29	29	9	80	77	9	80	80	5	53	9	80
76	78	5	28	5	32	30	32	31	31	51	51	32	78	51	51
51	31	51	51	51	51	78	76	51	51	32	32	76	32	32	33
33	33	32	78	29	31	80	31	31	33	33	31	80	33	31	33
33	27	77	53	80	27	53	29	80	53	53	29	53	29	28	54
4	28	52	53	52	27	27	27	28	27	30	54	54	79	29	29
4	28	28	79	54	78	54	54	78	54	76	78	76	55	78	76
76	55	55	76	81	81	81	55	5	55	77	5	78	78	5	77
5	77	5	77	81	81	75	75	81	81	75	81	75	81	77	75
56	75	75	79	56	75	79	79	57	57	56	79	57	79	79	57

1. Learned Policies are different from the observed policies in the training set.
2. Different hardware \Rightarrow Different learned policies.

Conclusion

- PoLe brings **Next Token Prediction** into the world of database optimization.
- It leverages **offline RL and DB-Tokens** to learn generalizable scheduling strategies.
- What's next?
 - Categorize the optimization tasks that can benefit from the Next Token Prediction (NTP) paradigm
 - Assess to what extent the PoLe framework can provide consistent performance and adaptivity guarantees

Thank You!

Questions?



Read our paper!



Get in touch!